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174/079Re

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Nghia Tran, Ying Xuan Li, Janusz Balicki  
and John Costello

Application No. : 10/084,757 Confirmation No.: 4411

Filed : October 19, 2001

For : SYSTEM FOR COUPLING PROGRAMMABLE LOGIC  
DEVICE TO EXTERNAL CIRCUITRY WHICH SELECTS A  
LOGIC STANDARD AND USES BUFFERS TO MODIFY  
OUTPUT AND INPUT SIGNALS ACCORDINGLY

Group Art Unit : 2819

Examiner : Daniel D. Chang

Hon. Commissioner for Patents  
Washington, D.C. 20231

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INFORMATION DISCLOSURE STATEMENT

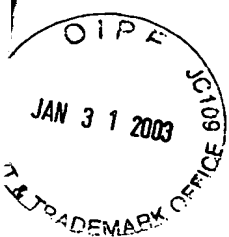
Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,  
applicants wish to call the attention of the Examiner to the  
following references:

U.S. Patents

Re. 34, 444	Kaplinsky	November 1993
4,032,800	Droscher et al.	June 1977
4,609,986	Hartmann et al.	September 1986
4,617,479	Hartmann et al.	October 1986
4,677,318	Veenstra	June 1987
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4,899,067	So et al.	February 1990
4,912,342	Wong et al.	March 1990
4,972,470	Farago	November 1990
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4,987,578	Akins et al.	January 1991
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5,003,200	Sakamoto	March 1991
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5,121,006	Pedersen	June 1992
5,121,359	Steele	June 1992
5,220,214	Pedersen	June 1993
5,235,219	Cooperman et al.	August 1993
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5,260,611	Cliff et al.	November 1993
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5,371,422	Patel et al.	December 1994
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5,412,599	Daniele et al.	May 1995
5,426,744	Sawase et al.	June 1995
5,428,305	Wong et al.	June 1995
5,483,178	Costello et al.	January 1996
5,534,798	Phillips et al.	July 1996
5,590,305	Terrill et al.	December 1996
5,600,267	Wong et al.	February 1997
5,732,407	Mason et al.	March 1998

R. C. Minnick, "A Survey of Microcellular Research,"  
Journal of the Association for Computing Machinery,  
Vol. 14, No. 2, pp. 203-41, April 1967.

S. E. Wahlstrom, "Programmable Logic Arrays -- Cheaper by  
the Millions," Electronics, December 11, 1967, pp. 90-95.

Recent Developments in Switching Theory, A. Mukhopadhyay,  
ed., Academic Press, New York, 1971, Chapters VI and IX,  
pp. 229-54 and 369-422.

These references are also listed on the attached Form  
PTO-1449, and copies of them are enclosed.

Consideration of the foregoing in relation to this  
patent application is respectfully requested.

Respectfully submitted,

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Docket No. 174/079Re

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Examiner : Daniel D. Chang

Hon. Commissioner for Patents  
P.O. Box 2327  
Arlington, VA 22202

New York, New York  
January 31, 2003

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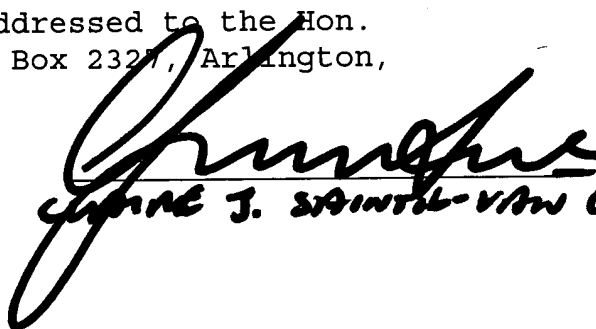
EXPRESS MAIL CERTIFICATION

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Date of Deposit January 31, 2003.

I hereby certify that this transmittal letter and the other papers and fees identified in this transmittal letter as being transmitted herewith are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and are addressed to the Hon. Commissioner for Patents, P.O. Box 2327, Arlington, Virginia 22202.

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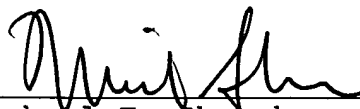
  
DANIEL D. CHANG

TRANSMITTAL LETTER

Sir:

Transmitted herewith for filing are a [X] Reply to Office Action; [X] Information Disclosure Statement (in duplicate); [X] Form PTO-1449 (in duplicate); and

[X] copies of all references cited, for the above-identified patent application.



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